



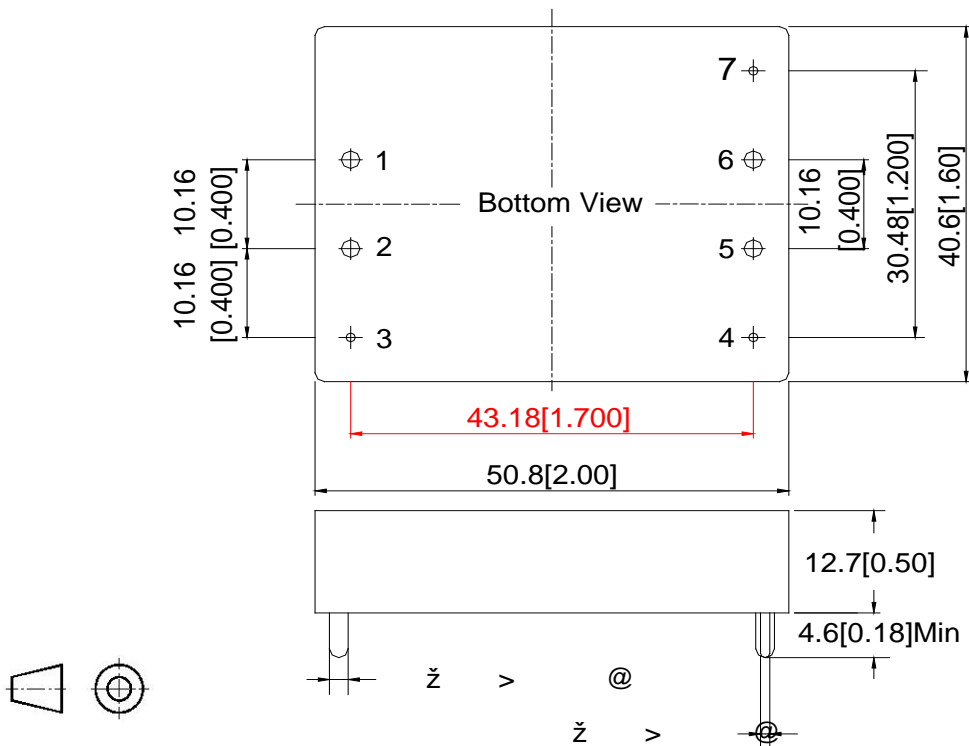
Features

- 1 50.8mmx40.6 mmx1 2.7mm
- 1 Input voltage range: 0 – 50 Vdc,
16.5-50Vdc work
- 1 Transient protection MIL-STD-1275 A/B/D and
MIL-STD-704A/E/F
- 1 EMI filtering
- 1 1500Vdc Input / Output to Base / FG
- 1 –40°C to 100°C baseplate operation



TSL50S10

Outline



Pin	Symbol	Function	Pin	Symbol	Function	Notes
1	+Vin	Input voltage positive	5	-Vo	Output voltage negative	Case material: Aluminium alloy; Pins material: Copper, tin-cerium plating; Units: mm(inches) ; Tolerance: X.X±0.5,(X.XX±0.02) X.XX±0.25(X.XXX±0.010)
2	-Vin	Input voltage negative	6	+Vo	Output voltage positive	
3	FG	EMI GND	7	NC	No connect	
4	Cnt	Control pin				



Specifications

Unless otherwise specified, all values are given at: 25°C
 connection.

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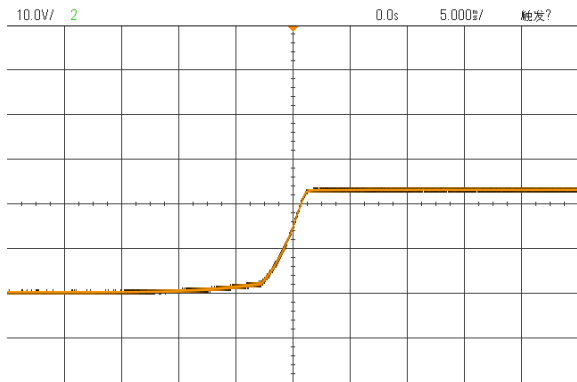
Input Specifications		Min	Typ	Max	Unit	Notes
Recommand input voltage range		18	24	36	V	Continuous
Working input voltage range		16.5	24	50	V	Continuous
Absolute input voltage range		-0.7	—	250	V	<i>With 125W max. limit</i> 250V:70 - s 100V:50ms 80V:100ms 70V:120ms 50V: Continuous <i>No load:</i> 250V:70 - s 100V: Continuous 600V:10 - s
External input capacitance		10	100	—	- F	FIG1 Cin
Negative logic	on	-0.7	0	0.3	V	Cnt refer to -Vo
	off	—	—	15	V	Cnt refer to -Vo; Cnt left:module off

Output Specifications		Min	Typ	Max	Unit	Notes
Output voltage range		16	24	49.5	V	
Internal voltage drop		—	—	1	V	
Output current		0	—	10	A	125W max. limit 8A: Continuous 10A:100ms after startup
External output capacitance		470	1000	—	- F	FIG1 Cout

General Specifications		Min	Typ	Max	Unit	Notes
Efficiency		—	98	—	%	Full load
Dielectric withstan		1500	—	—	Vdc	Input / Output to Base /FG,1minute,5mA
EMI		Common mode:26dB Different mode:35dB				
Case Temperature		-40	—	100	°C	
Storage Temperature		-55	—	125	°C	
Relative Humidity		10	—	90	%RH	
Manual Soldering		—	—	425	°C	5s max
Wave Soldering		—	—	260	°C	10s max
Weight		—	43	—	g	

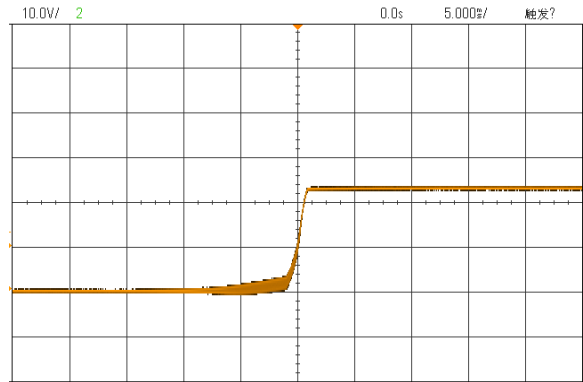
Characteristic Curves

Start-up



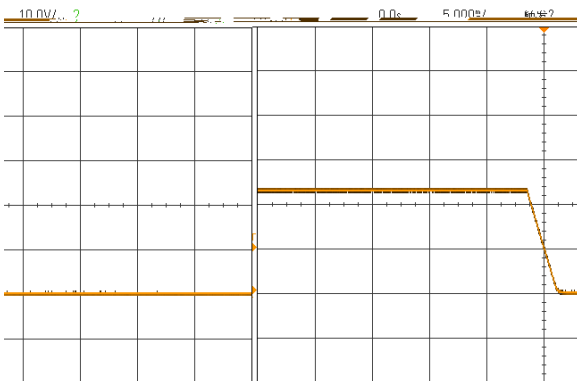
24V in, 8A output with 10- μ F input capacitance and 1000- μ F output capacitance

Start-up



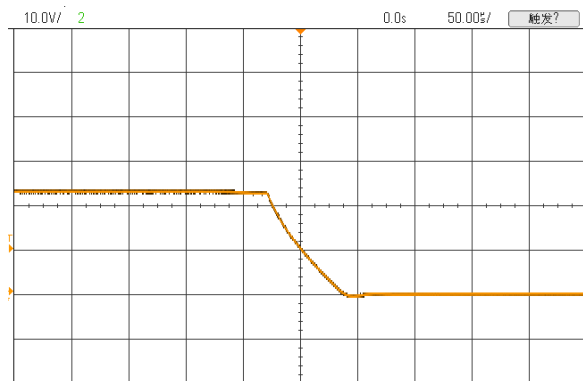
24V in, 8A output with 10- μ F input capacitance and 0- μ F output capacitance

Turn-off



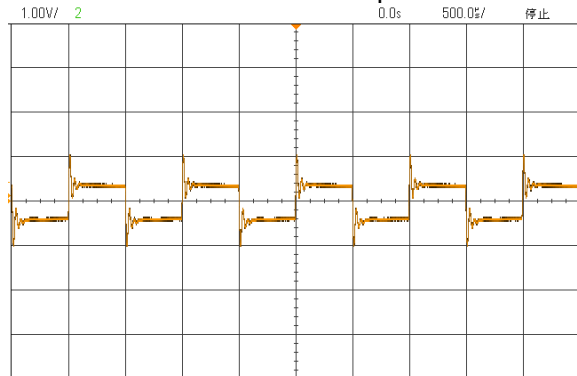
24V in, 8A output with 10- μ F input capacitance and 1000- μ F output capacitance

Turn-off



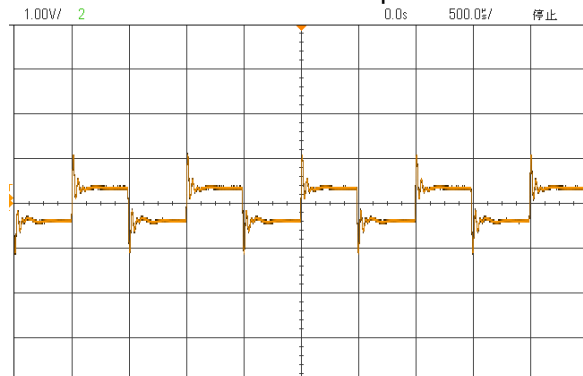
24V in, 8A output with 10- μ F input capacitance and 0- μ F output capacitance

Load Transient Response



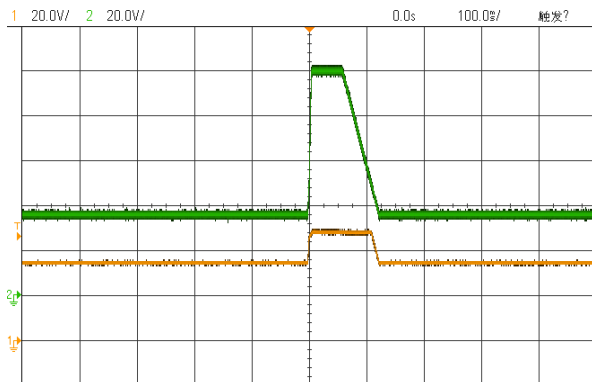
24V in, Load change 0A-5A-0A, 1A/ - s

Load Transient Response



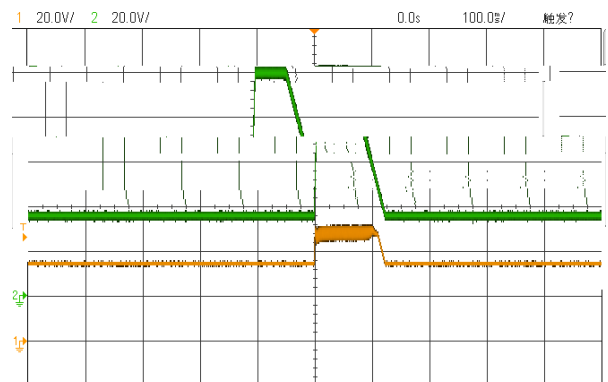
24V in, Load change 5A-10A-5A, 1A/ - s

Transient protect



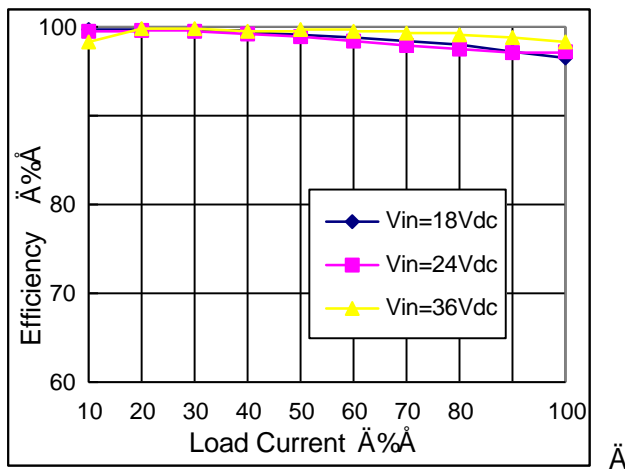
36V in, 100V pulse(60ms continuous, 60ms down to 36V) , with 10 - F input capacitance and 1000 - F output capacitance

Transient protect

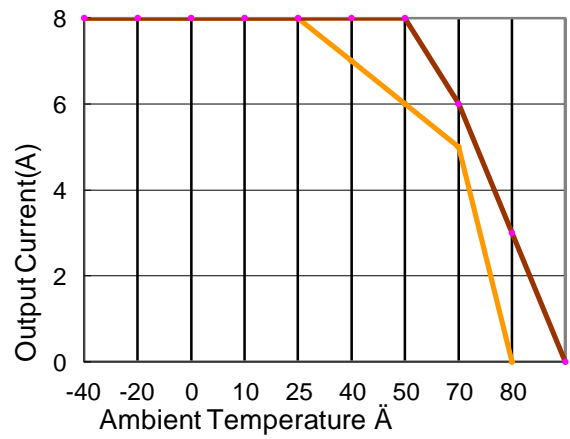


36V in, 100V pulse(60ms continuous, 60ms down to 36V) , with 10 - F input capacitance and 0 - F output capacitance

Efficiency Curve



Derating





2. There are two capacitors connected from FG to +Vin and –Vin in the TSL50S10.
3. Y-Cap will be selected by the EMI requirement.
4. Pin FG can be float according to de EMI requirement.

Cleaning Notice

The module case is not a hermetically-sealed construction, a sufficient drying process is required after the converter cleaning, make sure the liquid congregated is removed, or it will damage the converter or degradation of performance.

After surface treatment, the appearance of the converter may be affected by the organic solvent, protection measures should be taken before cleaning when appearance is concerned.

Contact Information

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